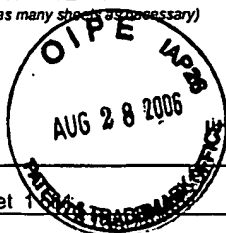


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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/081,818
Filing Date	February 20, 2002
First Named Inventor	Eldridge, Jerome
Group Art Unit	2818
Examiner Name	Ho, Tu-Tu

Sheet 1

Attorney Docket No: 1303.045US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
TH	US-20030042528A1	03/06/2003	Forbes, L.	08/30/2001
↓	US-20040032773A1	02/19/2004	Forbes, L.	08/08/2003
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↓	US-5,798,548	08/25/1998	Fujiwara, H.	05/17/1996
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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EXAMINER

/Tu Tu Ho/ (09/11/2006)

DATE CONSIDERED

09/11/2006

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached



S/N 10/081,818

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Jerome M. Eldridge et al.

Examiner: Tu-Tu V. Ho

Serial No.: 10/081,818

Group Art Unit: 2818

Filed: February 20, 2002

Docket: 1303.045US1

Title: ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW
ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS**COMMUNICATION CONCERNING RELATED APPLICATIONS****MS Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Noted Applicants would like to bring to the Examiner's attention the following related
TH applications in the above-identified patent application:

	<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/11/2006	11140643	May 27, 2005	1303.141US1	HAFNIUM TITANIUM OXIDE FILMS
	11202460	August 12, 2005	1303.027US3	SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
	11212190	August 26, 2005	1303.024US3	INTEGRATED CIRCUIT MEMORY DEVICE AND METHOD
	11380599	April 27, 2006	1303.045US3	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
	11471008	June 20, 2006	1303.035US3	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS
	11471007	June 20, 2006	1303.035US4	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS
	11471348	June 20, 2006	1303.035US5	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/081,818

Filing Date: February 20, 2002

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Dkt: 1303.045US1

Noted

Title: ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS

TH

09/11/2006

11492638	July 25, 2006	1303.019US4	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
11492637	July 25, 2006	1303.019US5	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

JEROME M. ELDRIDGE ET AL.

By Applicants' Representatives,

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Date

24 Aug '06

By

Timothy B. Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24 day of August, 2006.

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Name

Simon Rosendale
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